AMENDMENT AND RESPONSE UNDER 37 CFR § 1.111

Serial Number: 08/903,486 Filing Date: July 29, 1997

Title: SILICON CARBIDE GATE TRANSISTOR

Page 9 Dkt: 303.326US1

and

means for controlling current in the channel region based on an input

signal;

a row decoder coupled to the array;

a column decoder coupled to the array;

a voltage control switch; and

control circuitry coupled to the array to control read, write, and erase operations of the memory device.

REMARKS

In response to the Office Action dated 21 December 2000, the applicant respectfully requests reconsideration of the above-identified application in view of the following remarks. Claims 1-5, 8-15, 22, 24-29, 31-35, 37-48, and 50-53 are pending in the application. Claims 11-15, 22, 24-29, and 31-35 are allowable. Claims 1-5, 8-10, 37-48, and 50-53 are rejected. Claims 1-5, 8-10, 37-48, and 50-53 have been amended. New claims 55-57 have been added, and as a result claims 1-5, 8-15, 22, 24-29, 31-35, 37-48, 50-53, and 55-57 are now pending in this application. No new matter has been added.

Allowable Subject Matter

The Examiner indicated that claims 11-15, 22, 24-29, and 31-35 are allowable.

Rejections Under 35 U.S.C. §103

Claims 1, 2, and 8-10 were rejected under 35 USC § 103(a) as being unpatentable over Halvis et al. (U.S. Patent No. 5,369,040, Halvis). Claims 3-5, 37-48, and 50-53 were rejected under 35 USC § 103(a) as being unpatentable over Halvis, and further in view of Demichelis et al. The applicant respectfully traverses.

The applicant respectfully submits that the rejected claims 1-5, 8-10, 37-48, and 50-53 are allowable in view of the cited references. The applicant reserves the right to assert these claims in a continuation application.

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Page 10 Dkt: 303.326US1

However, the applicant has amended the rejected claims 1-5, 8-10, 37-48, and 50-53 to obtain a notice of allowance for the above-identified application. The applicant notes that the Examiner stated on page 3 of the office action that claims 11-15, 22, 24-29, and 31-35 were allowable "because the art does not disclose complementary FET's or memory devices using the claimed material." The applicant respectfully submits that the amended claims and the claims added herein fall within the subject matter that the Examiner indicated was allowable.

CONCLUSION

The applicant respectfully submits that all of the pending claims are in condition for allowance, and such action is earnestly solicited. The Examiner is invited to telephone the below-signed attorney at 612-373-6973 to discuss any questions which may remain with respect to the present application.

If necessary, please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

APR 2.5.2001 SERVICE TRADEMARK

Respectfully submitted,

LEONARD FORBES ET AL.

By their Representatives,

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Date AS Hpril 200

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<u>CERTIFICATE UNDER 37 CFR 1.8:</u> The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner of Patents, Washington, D.C. 20231, on this <u>23'</u> day of <u>April</u>, 2001.

Name HMY // 101

Signature

6